Amendments to the Specification

Please AMEND the Specification as follows:

Please REPLACE the first full paragraph on page 3 as follows:

According to the present invention, when the program processing unit judges that the address is not obtained from the label during the execution of the program, the label address translating unit obtains the address from the label and writes the obtained address to the program. Thus, the address obtained from the label is written to the program, and thereafter the program to which the address has been written is processed (compiled). Hence, there is no necessity for acquiring the address from the label each time the program is processed (compiled).

Please replace the first full paragraph on page 5 as follows:

The label address translating method according to the present invention may be structured to so that the third step involves reading the address to be obtained from a table showing a relationship between the label and the address.

Please replace the heading on page 6, line 24 as follows:

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Please replace the third paragraph on page 7 as follows:

The CPU 12 executes the label address translation program stored in a storage unit such as a ROM, a hard disk and a magnetic disk (none of those storage devices is shown herein, and the storage device corresponds to a readable-by-computer medium according to the present invention), thereby functioning as a label address translating unit (corresponding to a label address translating module) as well as a program executing unit 16 (corresponding to a program processing module).

Please replace the fourth full paragraph on page 8 extending over to page 9 as follows:

Note that what is typical as a program edit tool of the interpreter language at the present is that an intermediate code is compiled for the duration of a compilation of a source program by the user in order to increase an processing speed in a posterior execution process. The compile process is carried out during the editing of the source program also in the edit tool of the

interpreter language in this embodiment.

Please replace the third full paragraph on page 9 as follows:

Further, the program ~~execution~~ executing unit 16 executes a process of referring to the address during the execution of the program 20. For instance, the program ~~execution~~ executing unit 16 performs a certain arithmetic operation and stores an arithmetic result thereof in a predetermined address of the memory. At this time, the program ~~execution~~ executing unit 16, when recognizing that an invalid piece of data (invalid data) is described in the program 20, the exception handling (i.e., the label address translation process) other than the normal processes of the program 20.

Please replace the first paragraph on page 10 as follows:

The label address translating unit 18, when executing the exception handling, starts up an exception ~~handling~~ handler 22. The exception handler 22 retrieves the label table 32, in which an address of a command (e.g., [FSTP]) becoming a target for the exception handling and the label described in the source code 28 of the program, serve as a key. The exception handler 22 thus reads the relevant effective address from the label table 32.

Please amend the Abstract as follows:

A label address translating device includes a ~~program processing~~ unit for processing a program and judging, when processing the program, whether or not an address is obtained from a label, and a label address translating unit for reading the address corresponding to the label from a label table in an exception handling, and writing the address to the program. ~~the~~ The program‚ after being processed by the label address translating device‚ comes into a state where the label is rewritten into the address. The rewriting into the address is performed in the exception handling, and a time needed for processing (compiling) the program ~~can therefore be~~ is reduced.